

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>			Docket Number (Optional) NSC1-G2511 [P04437 D2]		Application Number <u>10-736194</u>	
			Applicant(s) Peter J. Hopper			
			Filing Date <u>10-16-02</u>	Group Art Unit <u>3825</u>		

### U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
NL	*AA	5,128,737	07/07/92	van der Have	257	503	03/02/90
NL	*AB	5,635,753	06/1997	Hofflinger et al.	257	621	12/30/92
NL	*AC	5,734,187	03/31/98	Bohr et al.	257	377	06/16/97
NL	*AD	6,084,304	07/04/00	Huang et al.	257	762	06/05/98
NL	*AE	6,136,640	10/24/00	Marty et al.	438	239	07/17/98
NL	*AF	5,164,334	11/17/92	Mizushima	437	203	12/26/90
NL	*AG	2001/0002072	05/31/01	Ireland	257	758	12/15/00

### FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
NL	*AH	JP-11-352512	12/24/99	Japan	G02F	1/136	X	

### OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

NL	*AI	Stanley Wolf, "Silicon Processing for the VLSI ERA", Volume 2: Process Integration, 1990, pp. 183-186.

Examiner	<i>Nani Cen</i>	Date Considered	<i>10/25/04</i>
Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			